Electronic Acknowledgement Receipt				
EFS ID:	4540363			
Application Number:	10591001			
International Application Number:				
Confirmation Number:	2456			
Title of Invention:	High-Voltage Pmos Transistor			
First Named Inventor/Applicant Name:	Martin Knaipp			
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Filer:	Thomas Langer/YOLANDA USHER			
Filer Authorized By:	Thomas Langer			
Attorney Docket Number:	5367-257PUS			
Receipt Date:	30-DEC-2008			
Filing Date:	25-JUN-2007			
Time Stamp:	20:00:29			
Application Type:	U.S. National Stage under 35 USC 371			
Payment information:				

no

Submitted with Payment

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	1 Response to Election / Restriction Filed		21917	no	2
'			49509(0ec3efaxa5c83bede3c72a12ae9705 9ae2		

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## New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

## New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.